



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Janardhanan S. AJIT

Appl. No.: 10/617,874

Filed: July 14, 2003

For: **I/O Circuit Using Low Voltage
Transistors Which Can Tolerate
High Voltages Even When Power
Supplies Are Powered Off**

Confirmation No.: 3434

Art Unit: 2816

Examiner: To Be Assigned

Atty. Docket: 1875.2120003

Petition for Extension of Time Under 37 C.F.R. § 1.136(a)(1)

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

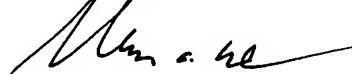
It is hereby requested that the period for replying to the outstanding Office Action be extended three (3) months from December 8, 2003 to March 8, 2004 by the filing of this Petition and fee payment.

The petition fee (37 C.F.R. § 1.17(a)) is believed to be \$950.00 for a three (3) month extension for a large entity. Fee payment is provided in our accompanying Credit Card Payment Form PTO-2038. However, if extensions of time under 37 C.F.R. § 1.136 other than those provided herewith are required to prevent abandonment of the present patent application, then such extensions of time are hereby petitioned.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



Theodore A. Wood
Attorney for Applicant
Registration No. 52,374

Date: March 3, 2004

1100 New York Avenue, N.W.
Washington, D.C. 20005-3934
(202) 371-2600

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